

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

James M. Derderian

Serial No.: 09/938,106

Filed: August 23, 2001

For: METHODS FOR ASSEMBLING
SEMICONDUCTOR DEVICES WITH
DISCRETE CONDUCTIVE ELEMENTS
SEPARATING ADJACENT,
SUPERIMPOSED SEMICONDUCTOR
DEVICES

Confirmation No.: 1038

Examiner: J. Im

Group Art Unit: 2811

Attorney Docket No.: 2269-4832US
(2001-0104.00/US)

VIA ELECTRONIC FILING

June 24, 2008

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed March 24, 2008, the three-month shortened statutory period for response to which expires on June 24, 2008.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

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Remarks begin on page 10 of this paper.